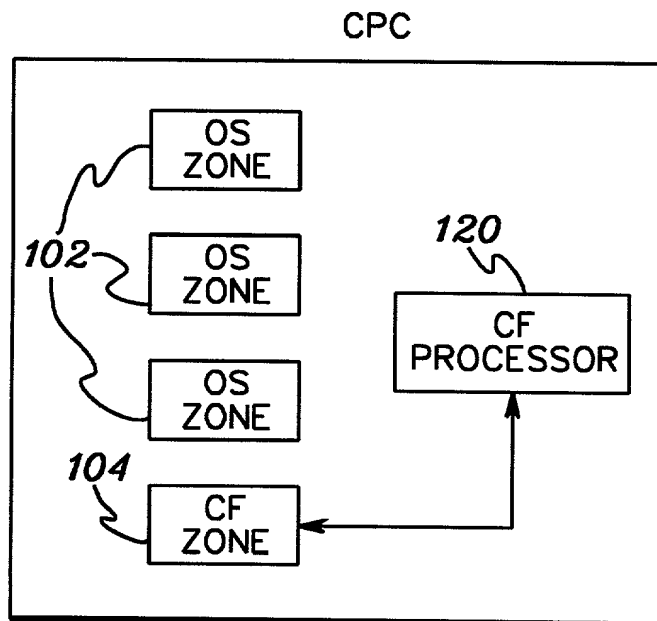
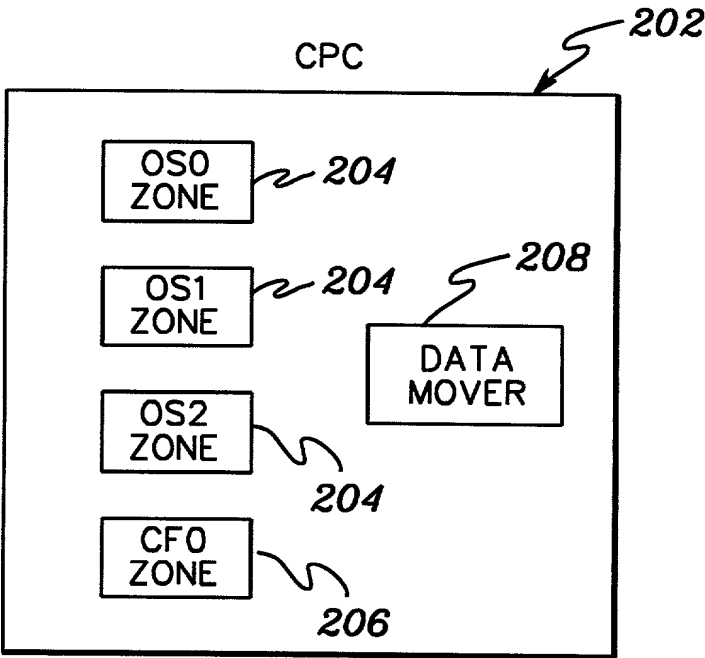


*fig. 1A*  
(PRIOR ART)



*fig. 1B*  
(PRIOR ART)

200



*fig. 2*

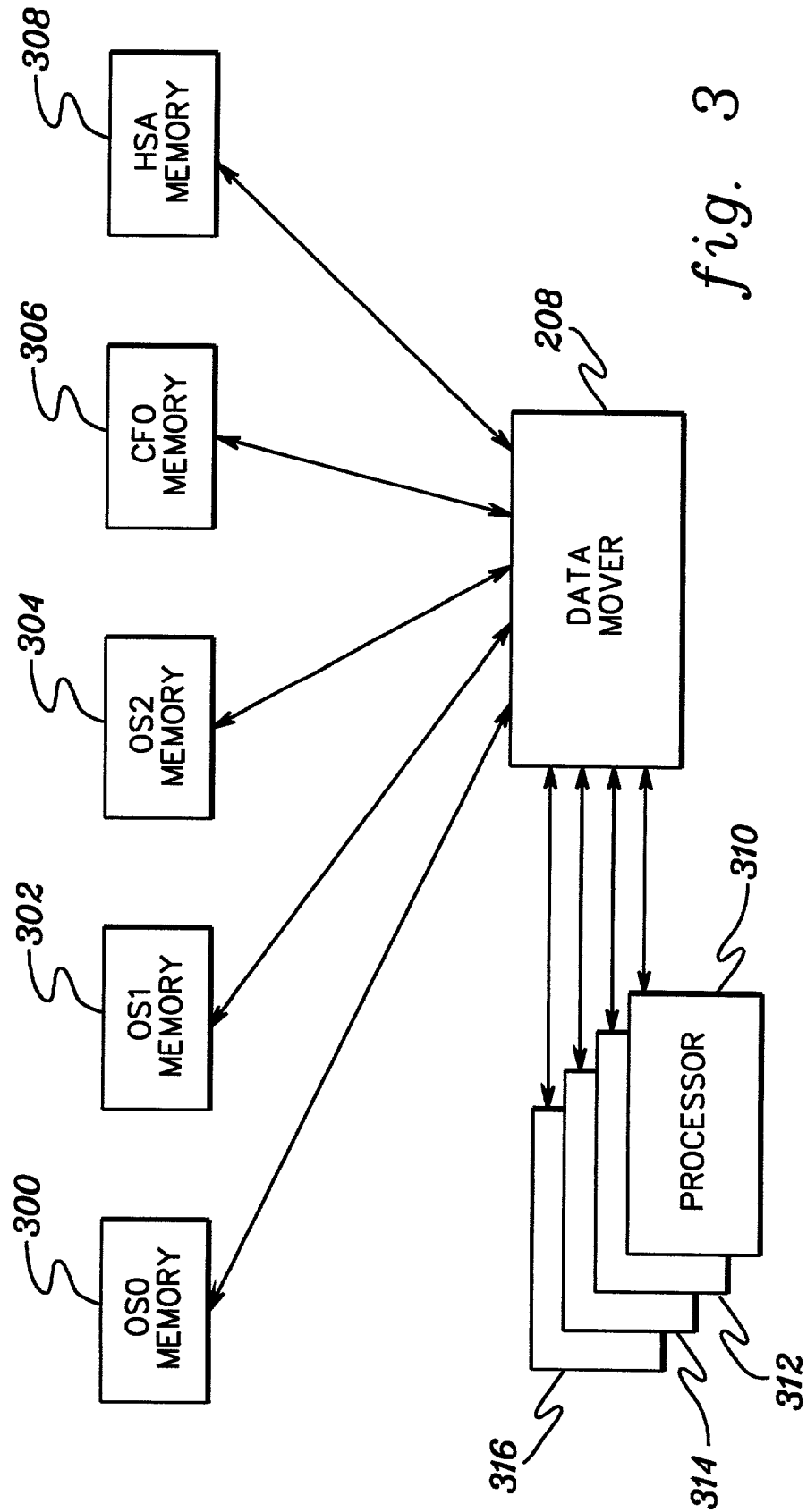
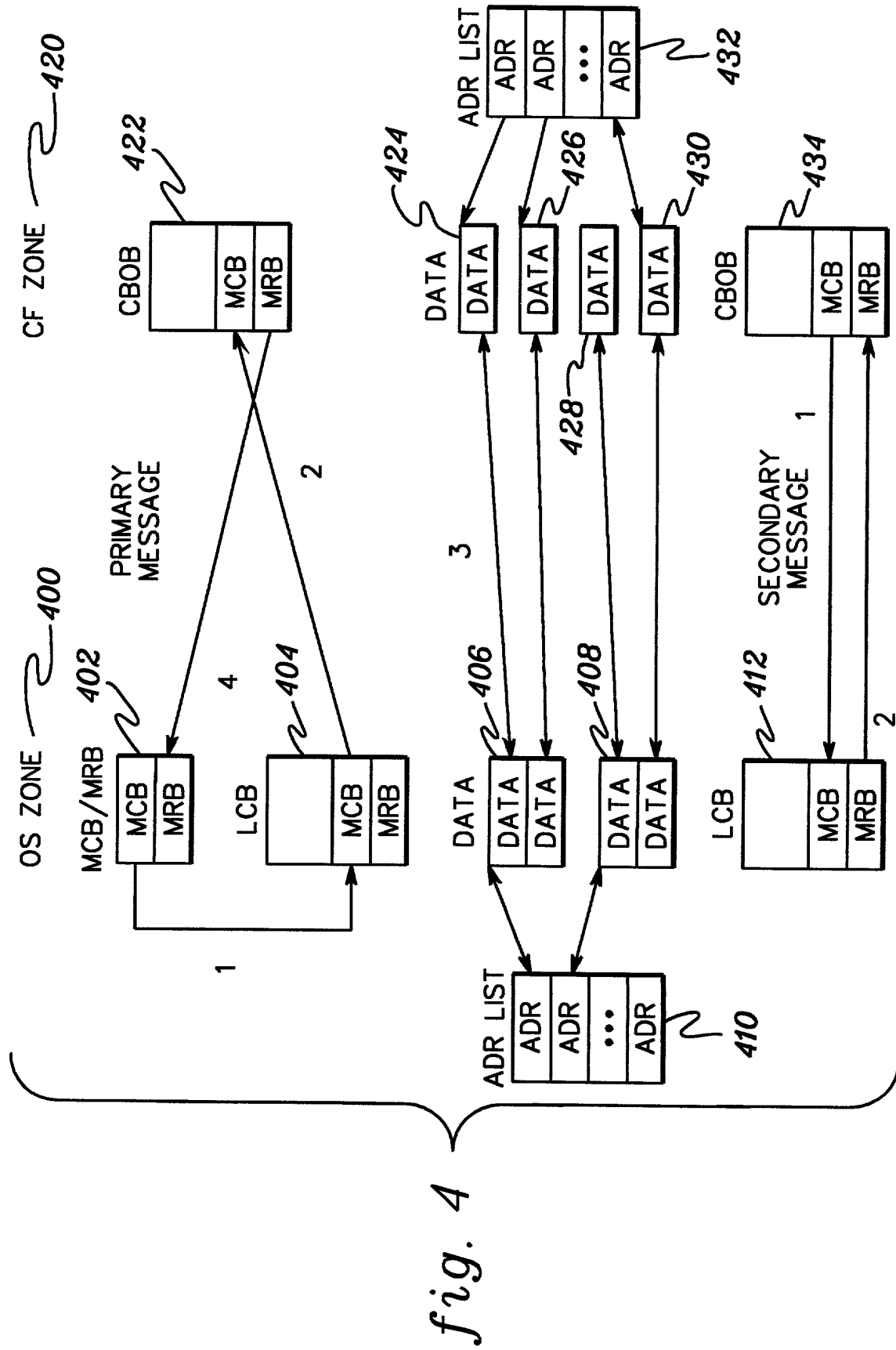
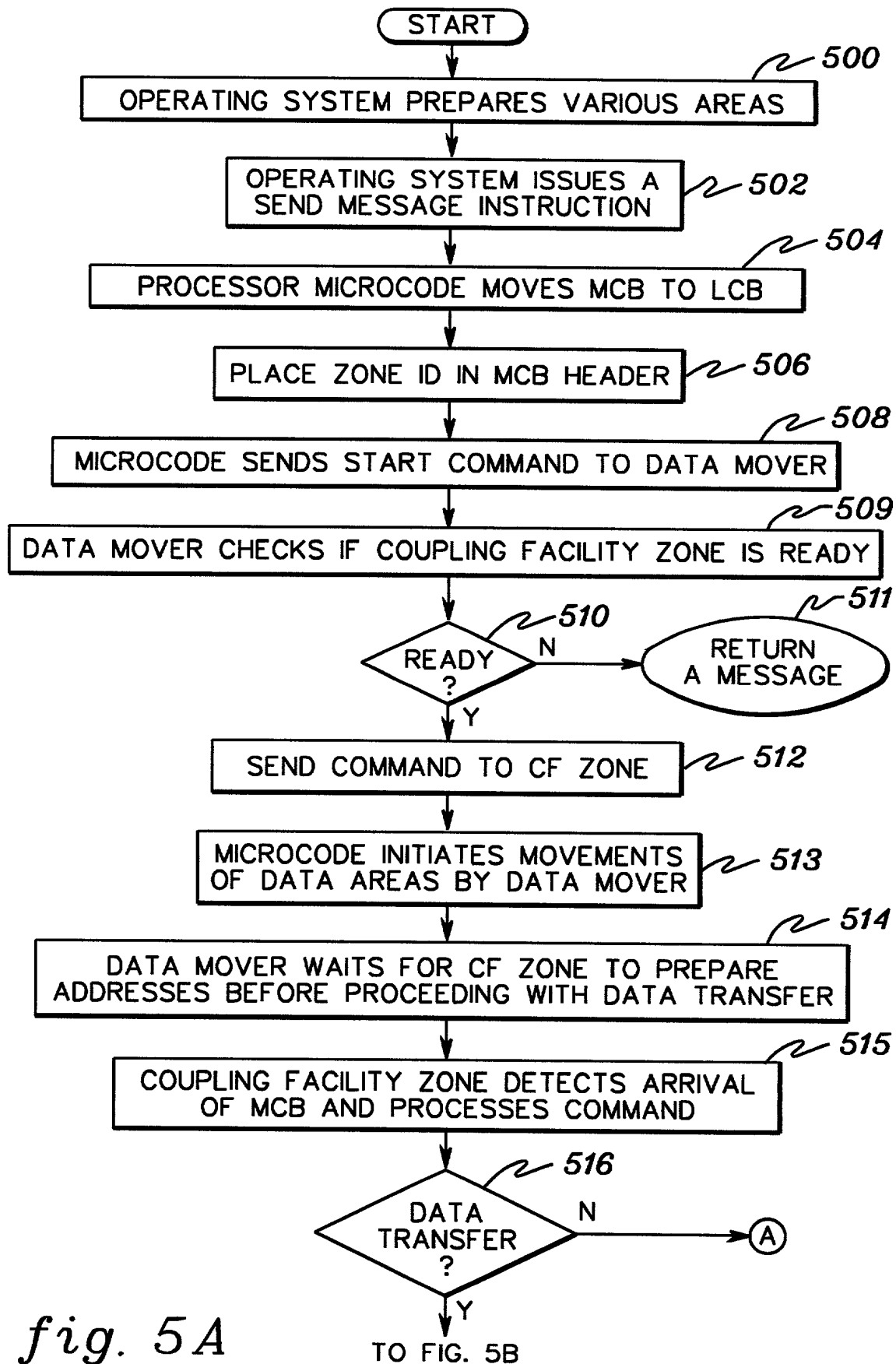
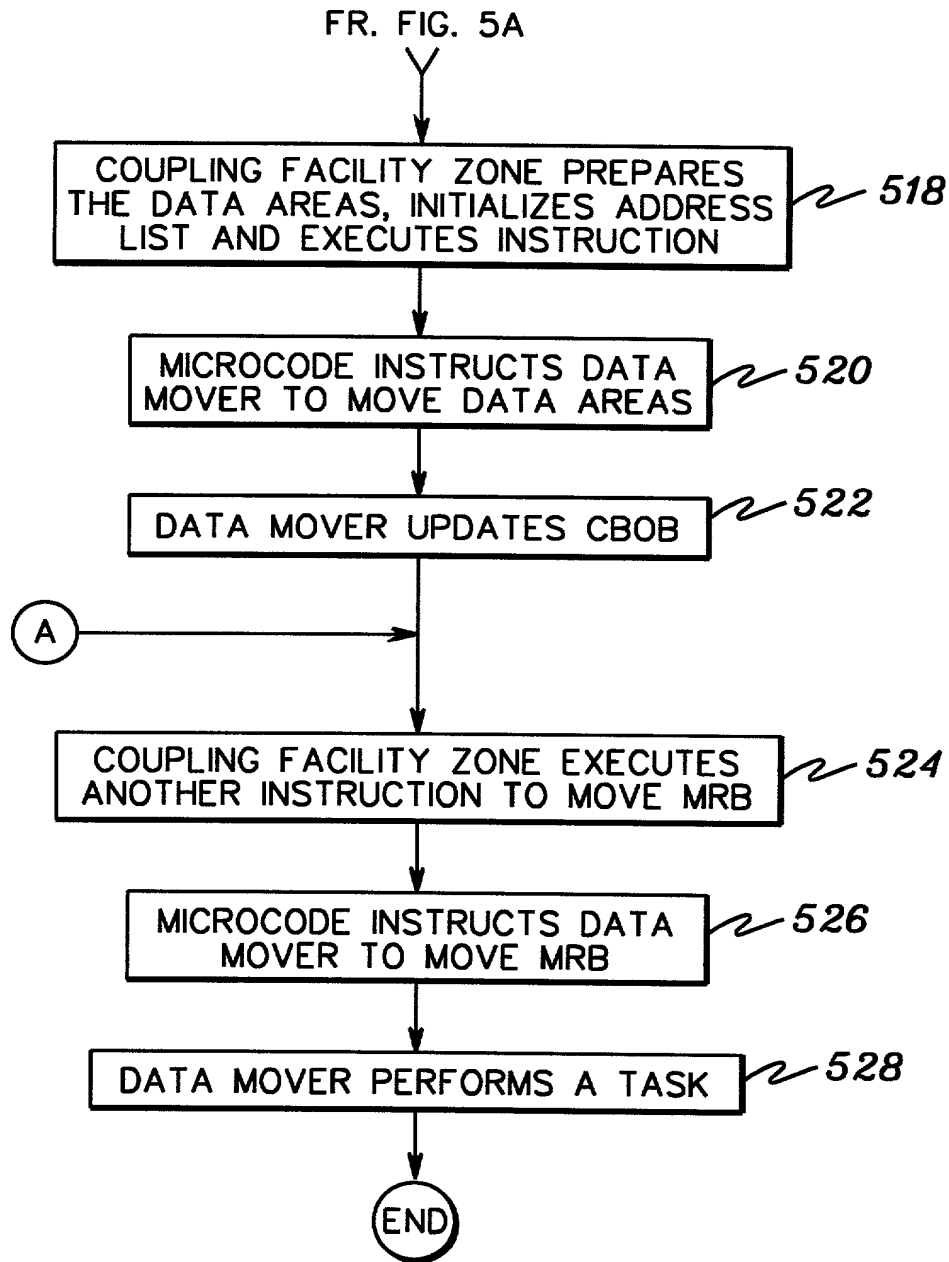


fig. 3

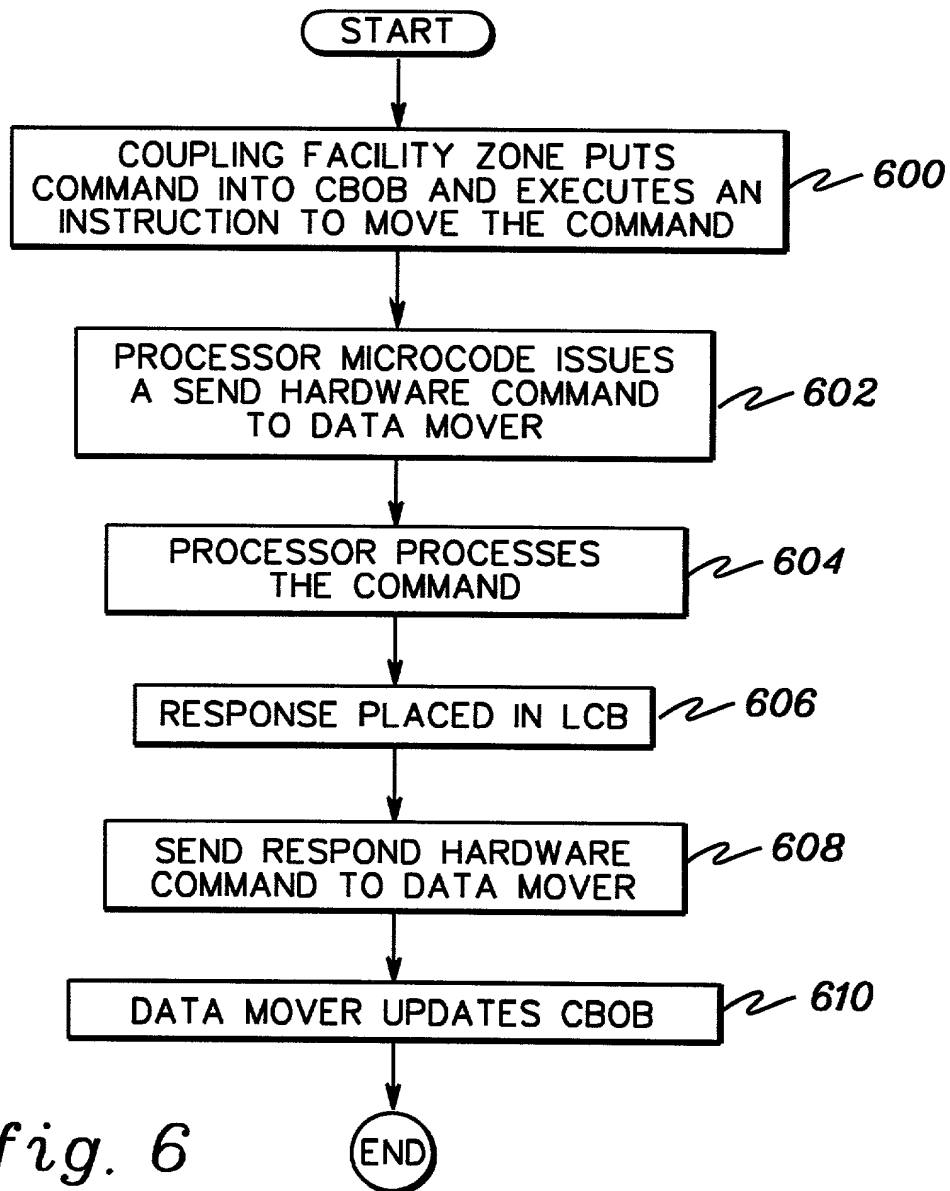
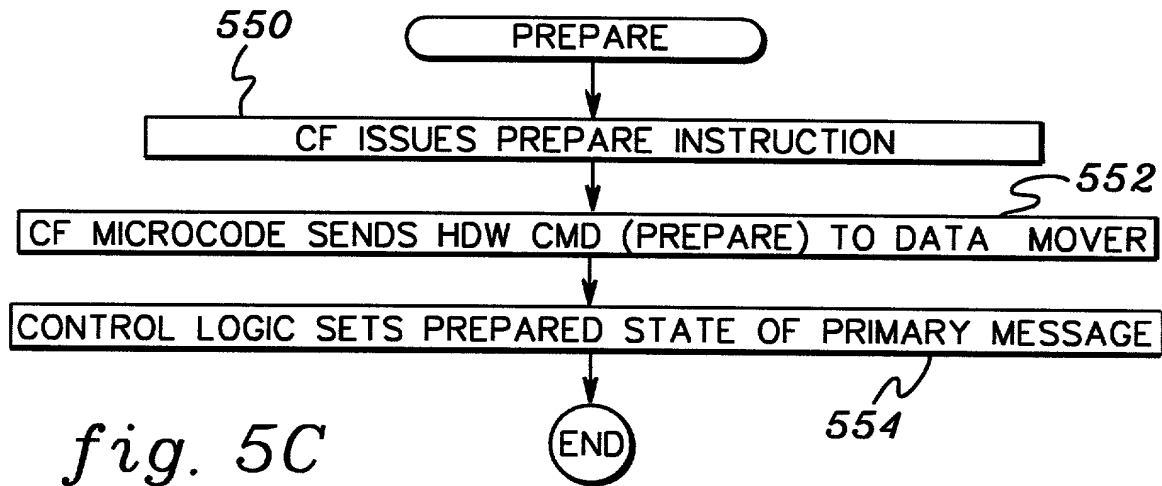
FIG. 4 is a block diagram of a system architecture for a primary message and a secondary message. The system includes an OS ZONE 400 and a CF ZONE 420. The OS ZONE 400 contains an MCB/MRB 402 and an LCB 404. The CF ZONE 420 contains a CBOB 422. The MCB/MRB 402 and the LCB 404 are connected to the CBOB 422 via a primary message 1 and a secondary message 2. The primary message 1 is a data path from the MCB/MRB 402 to the CBOB 422. The secondary message 2 is a data path from the LCB 404 to the CBOB 422. The system also includes an ADR LIST 410 and an ADR LIST 432. The ADR LIST 410 is connected to the MCB/MRB 402 and the LCB 404 via a data path 406. The ADR LIST 432 is connected to the CBOB 422 via a data path 430. The ADR LIST 410 and the ADR LIST 432 are connected to each other via a data path 428. The ADR LIST 410 and the ADR LIST 432 are also connected to each other via a data path 426. The ADR LIST 410 and the ADR LIST 432 are also connected to each other via a data path 424.







*fig. 5B*



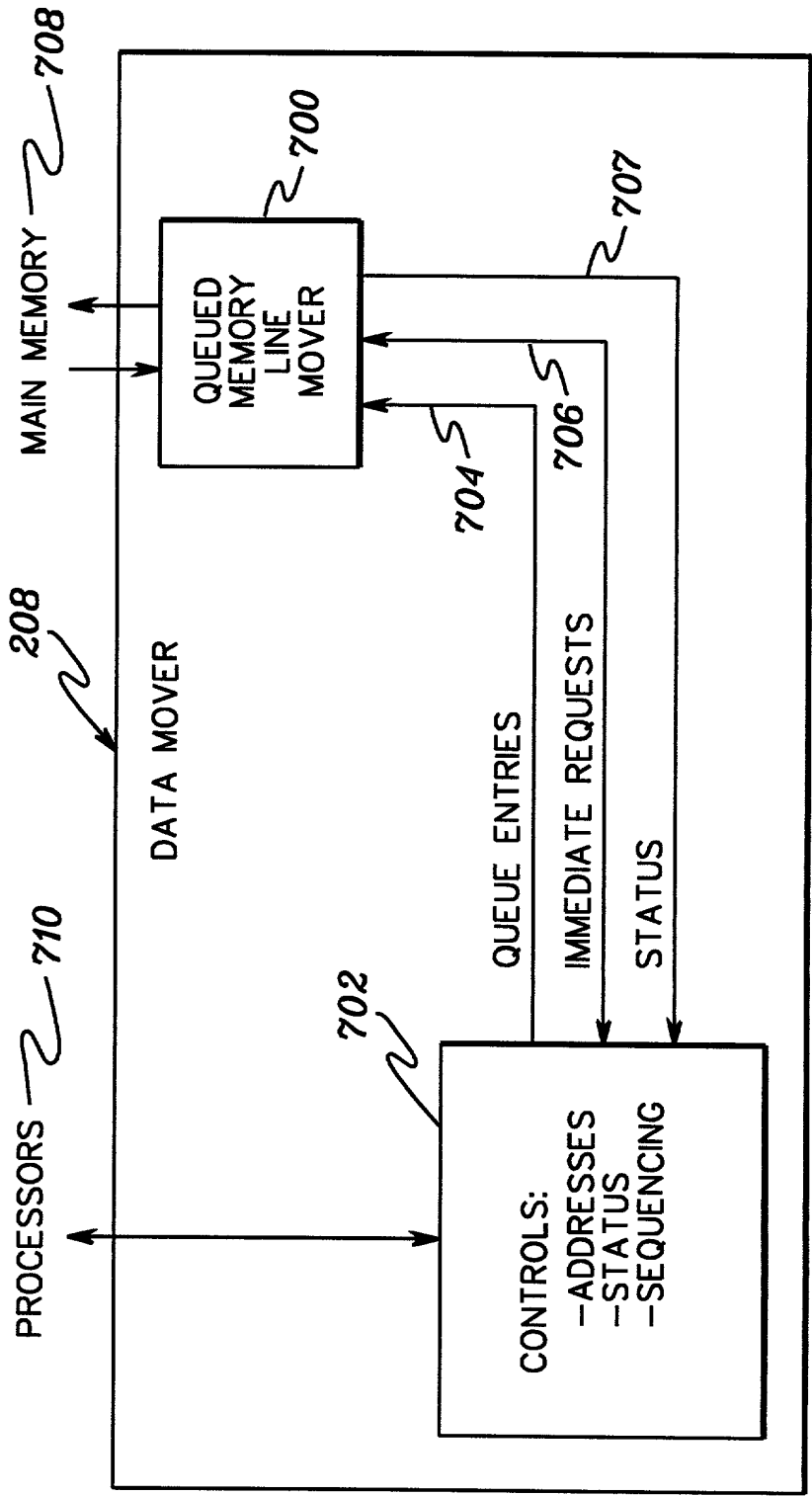


fig. 7



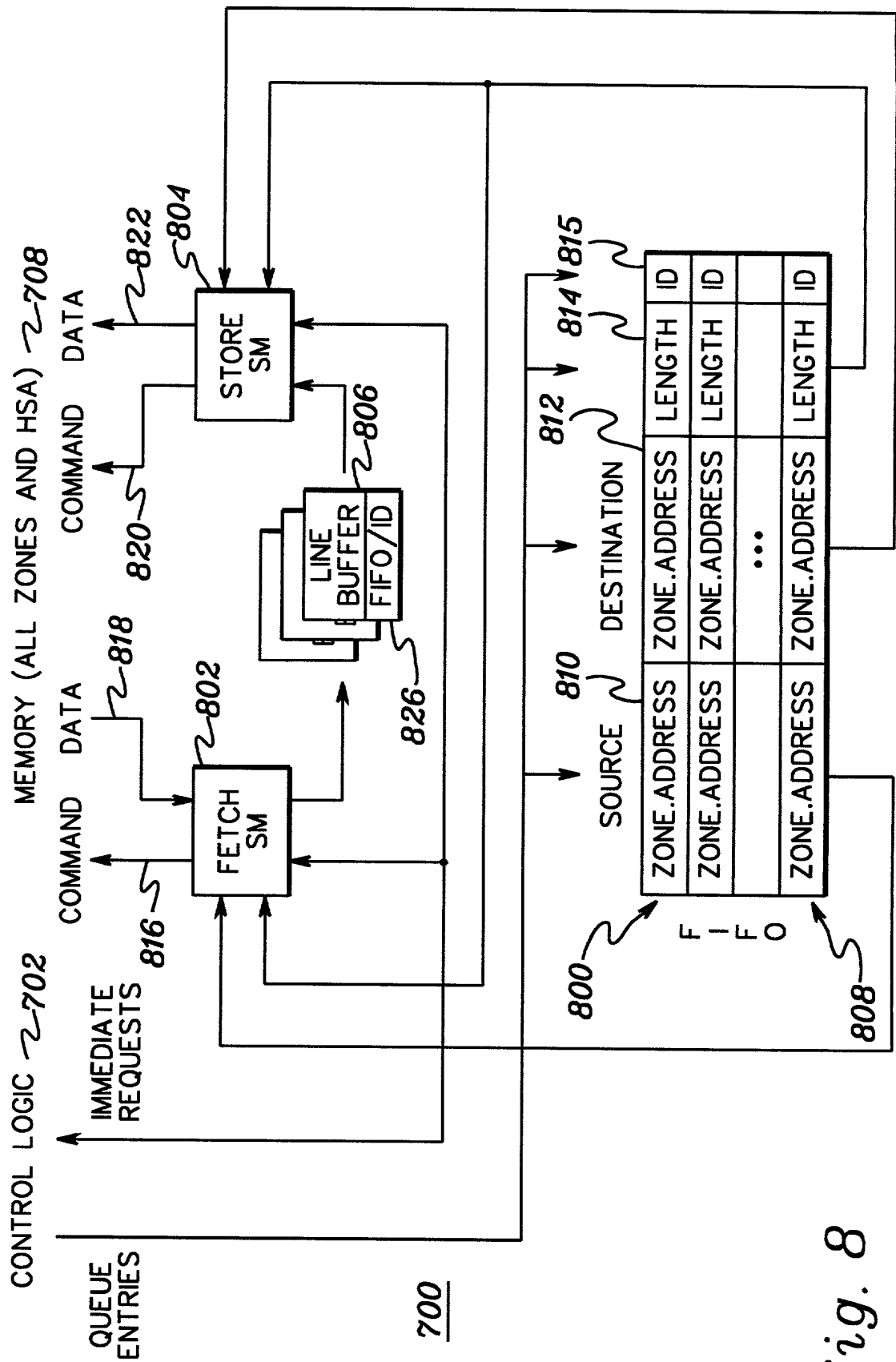
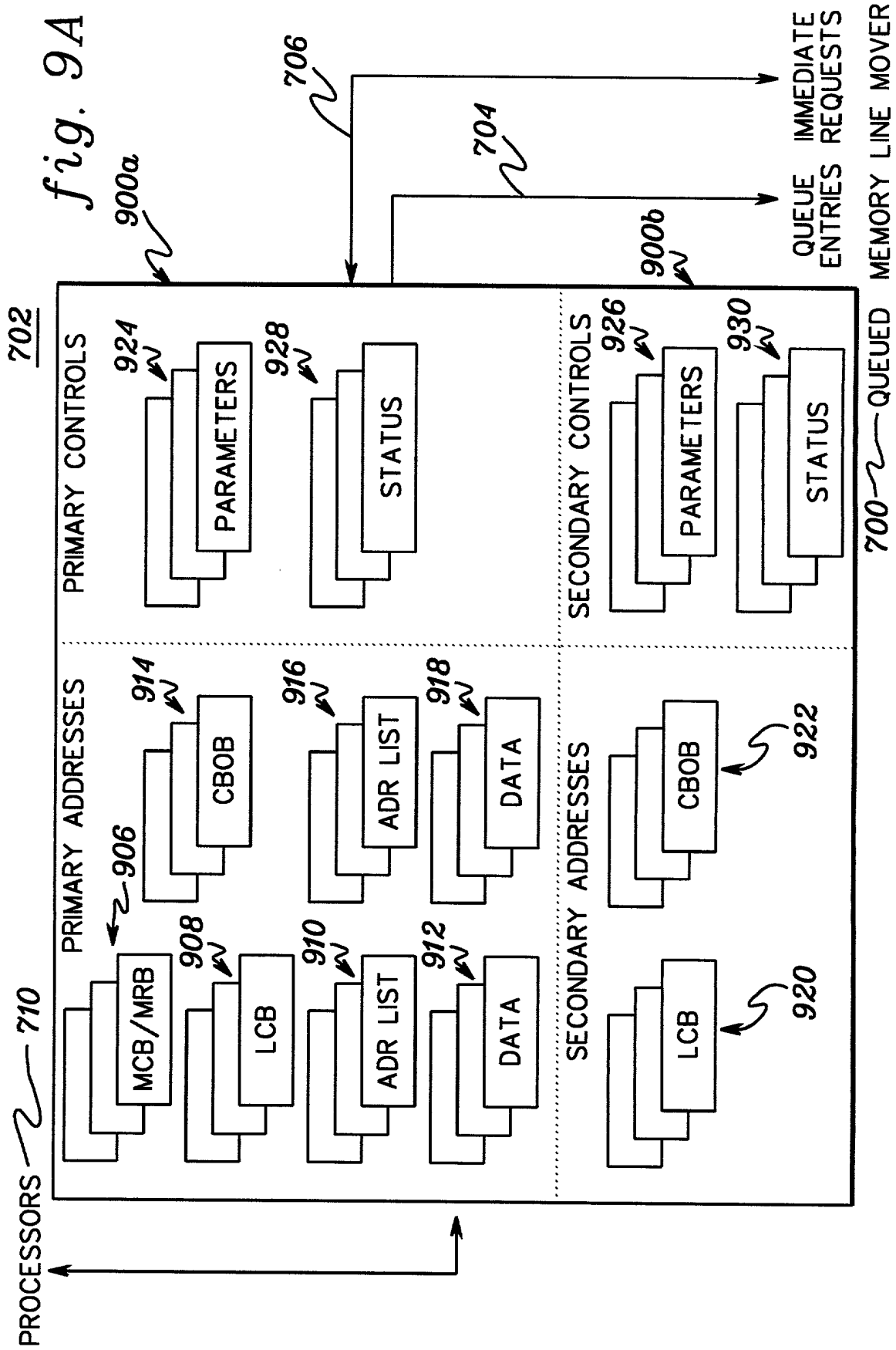


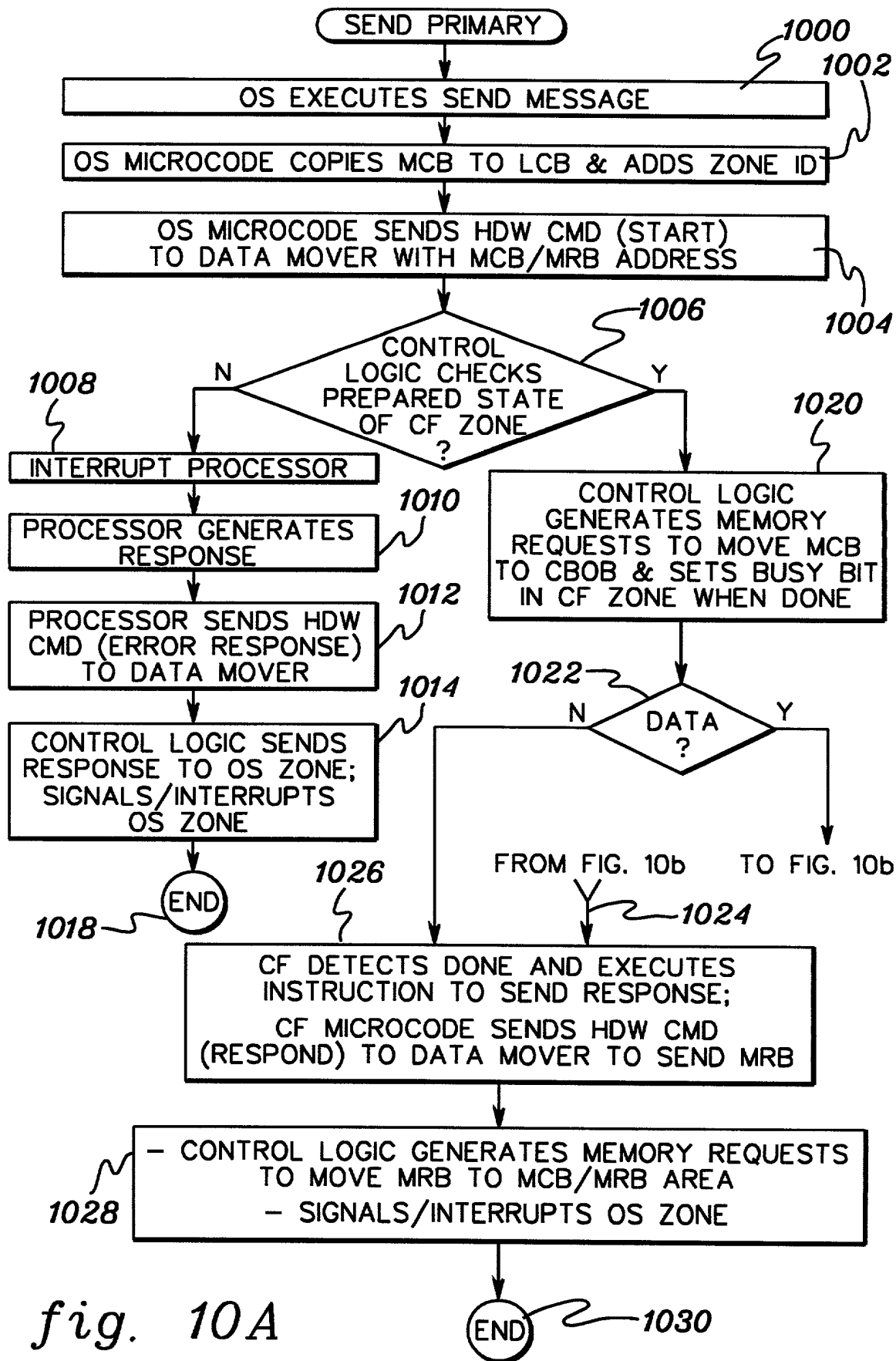
fig. 8

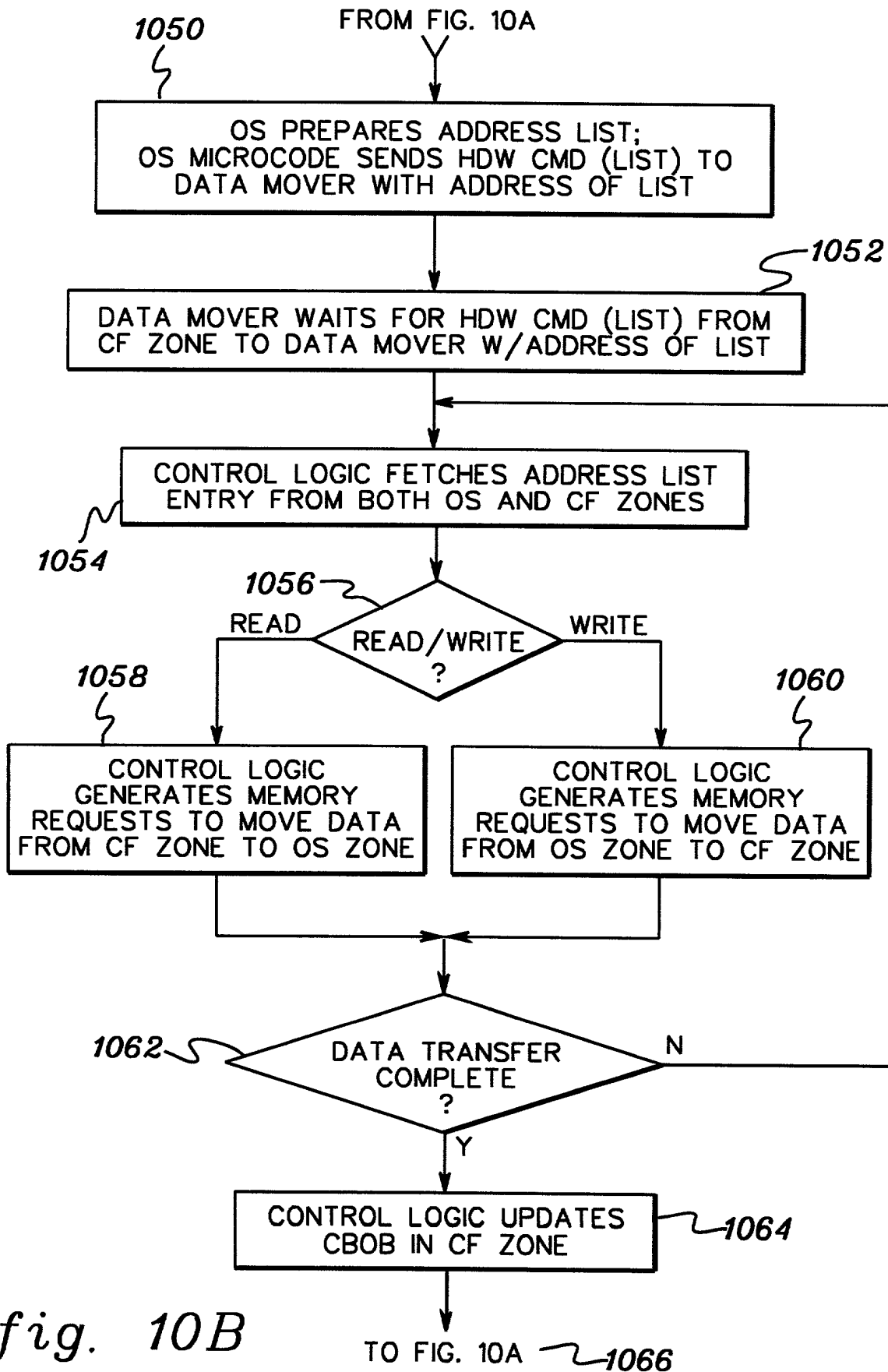


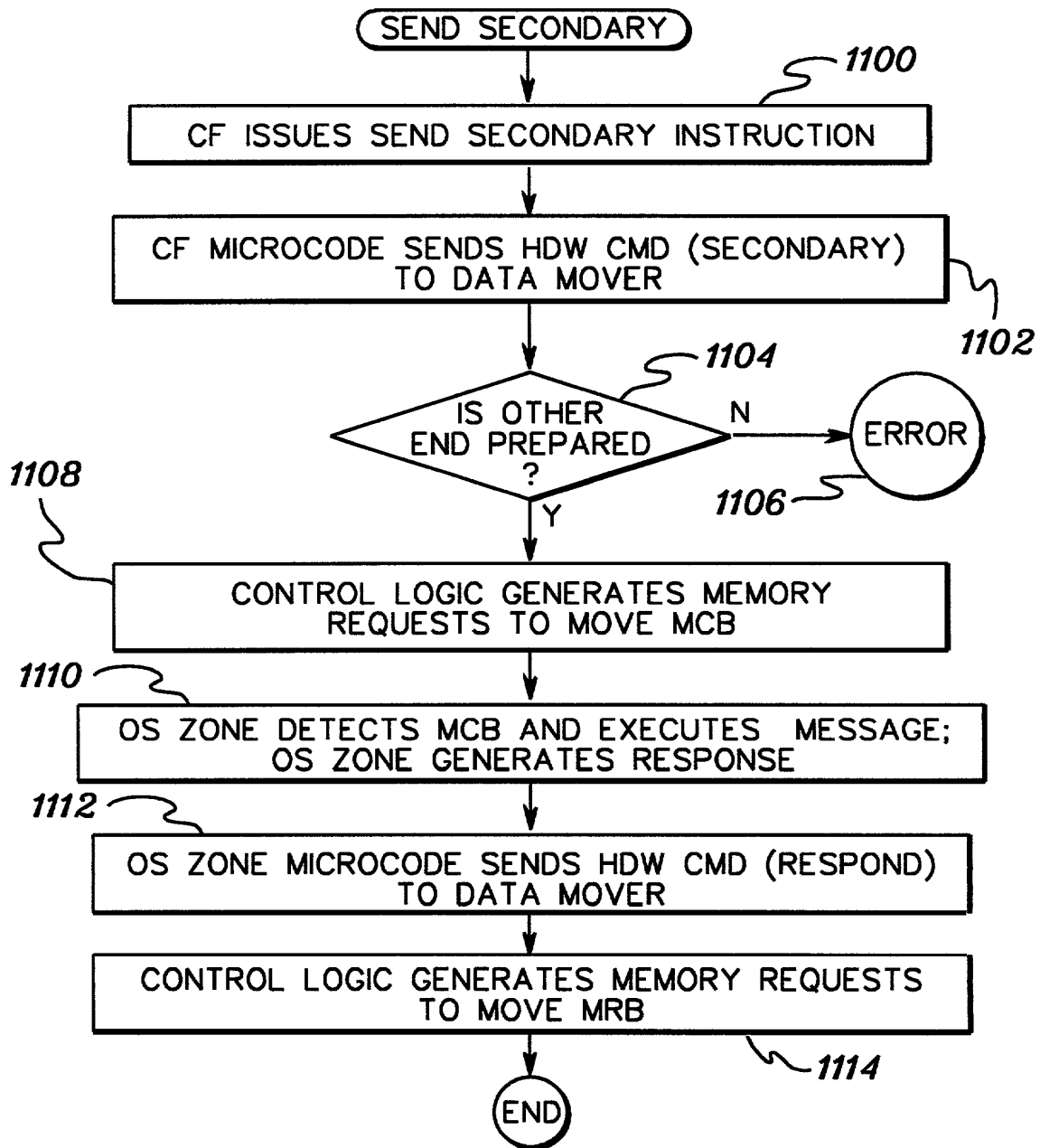
	0	1	2	3	950
0					MESSAGE SEGMENT
1					MESSAGE SEGMENT
2					MESSAGE SEGMENT
3					MESSAGE SEGMENT

952

*fig. 9B*







*fig. 11*